


Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DOCKET NO: 5150-61502 		SERIAL NO: 10/058,150			
		APPLICANT: Odom et al.					
		FILING DATE: October 29, 2001		GROUP: 2123			
U.S. PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
FOREIGN PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
10	D1	DE 692 32 869 T2	Sept. 4, 2003	Germany			YES
10	D2	DE 42 05 524 A1	Aug. 27, 1992	Germany			YES
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
10	D3	Lauwereins, R; Engels, M; Ade, M; Peperstraette, J; Grape-II: A System-Level Prototyping Environment for DSP Applications, Computer, Vol. 28, Issue 2, pp. 35-43, 2/95.					
10	D4	Lysaght, P; Stockwood, J; A Simulation Tool for Dynamically Reconfigurable Field Programmable Gate Arrays, IEEE Transactions on Very Large Scale Integration Systems, Vol. 4, Issue 3, pp. 381-390, 9/96.					
10	D5	Petronino et al., An FPGA-based Data Acquisition System for a 95 GHz. W-band Radar, IEEE International Conference on Acoustics, Speech and Signal Processing, Vol. 5, pp. 4105-4108, April 1997.					
10	D6	Boulay et al., A High Throughput Controller for a 256-Channel Cardiac Potential Overlapping System, Canadian Conference on Electrical and Computer Engineering, Vol. 1, pp. 539-542, September 1995.					
EXAMINER: 108hal			DATE CONSIDERED: 3/3/05				
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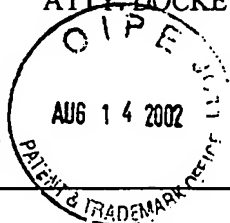
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KG	A1	4,849,880	7/18/89	Bhaskar et al.			
	A2	4,901,221	2/13/90	Kodosky et al.			
	A3	5,109,504	4/28/92	Littleton			
	A4	5,437,464	8/1/95	Terasima et al.			
	A5	5,497,498	3/5/96	Taylor			
	A6	5,535,342	7/9/96	Taylor			
	A7	5,555,201	09/10/1996	Dangelo et al.			
	A8	5,566,295	10/15/96	Cypher et al.			
	A9	5,583,749	12/10/96	Tredennick et al.			
	A10	5,603,043	02/11/1997	Taylor et al.			
	A11	5,603,043	2/11/97	Taylor et al.			
	A12	5,652,875	7/29/97	Taylor			
	A13	5,684,980	11/4/97	Casselman			
	A14	5,732,277	03/24/1998	Kodosky et al.			
	A15	5,784,275	7/21/98	Sojoodi et al.			
	A16	5,847,955	12/98	Mitchell et al.			
	A17	6,219,628	04/2001	Kodosky et al.			
	A18	6,226,776	05/01/2001	Panchul et al.			
	A19	6,230,307	05/082001	Davis et al.			
KG	A20	6,311,149	10/30/2001	Ryan et al.			


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EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
KG	A21	WO 94 10627 A	5/11/94	PCT			
	A22	WO 94 15311 A	7/7/94	PCT			
	A23	WO 96 14618 A	5/17/96	PCT			

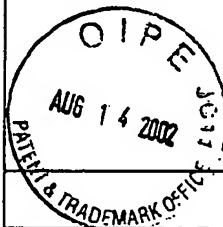
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)					
13	A24	Douglas, Bruce D., Ph.D., "Custom Embedded Communication Protocols, Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 637-657.			
	A25	Labrosse, Jean, "Inside Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 205-214.			
	A26	Labrosse, Jean, "Designing with Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 379-389.			
	A27	Operating Systems, 2 nd Ed. By Williams Stallings, Prentice Hall, 1995, "Multiprocessor and Real-Time Scheduling", pp. 394-415.			
	A28	International Search Report for PCT/US 98/10916 dated 9/17/98.			
	A29	"LabVIEW USER MANUAL for Windows", National Instruments Co., pp. 4/1-21.			
	A30	Beguelin et al., "Visualization and Debugging in a Heterogenous Environment," IEEE, pp. 88-95.			
	A31	Jamal et al., "The Applicability of the Visual Programming Language LabView to Large Real-World Application," IEEE, pp. 99-106.			
	A32	Per-Arne Wiberg, "Graphical Programming of Time-Deterministic Real-Time Systems," IEEE, pp. 173-180.			
	A33	XP0002087124 Xiao-Yu, et al., "Software Environment for WASMII: A Data Driven Machine with a Virtual Harware," Field Programmable Logic Architectures, Synthesis and Applications, 4 th International Workshop on Field-Programmable Logic and Applications, FPL '94 Proceedings, Berlin Germany, 1994.			
	A34	XP000554820 Edwards, et al., "Software acceleration using programmable hardware devices," IEEE Proceedings: Computers and Digital Techniques, Vol. 143, No. 1, January 1996, pp. 55-63.			
	A35	XP000380758 Lesser, et al., "High Level Synthesis and Generating FPGAs with the BEDROC System," Journal of VLSI Signal Processing, Vol. 6, No. 2, August 1993, pp. 191-214.			
	A36	International Search Report for PCT/US 98/13040 mailed 12/22/98.			
	A37	Ade, M; Lauwereins, R; Peperstraete, J.A.; Hardware-Software Codesign with GRAPE, Proceedings of the Sixth IEEE International Workshop on Rapid System Prototyping, pp. 40-47, June 1995.			
13	A38	De Coster, GRAPE-II: An Introduction [online]. Automatic Control and Computer Architectures Department. Katholieke Universiteit Leuven, Belgium, February 22, 1996 [retrieved October 6, 1999] Retrieved from the Internet @ http://www.esat.kuleuven.ac.be/acca .			

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C9	A39	Wenban et al., A Software Development System for FPGA-based Data Acquisition Systems, Proceedings of the IEEE Symposium on FPGAs for Custom Computing Machines, pp. 28-37, April 1996.
	A40	Collamati et al. "Induction Machine Stator Fault On-line Diagnosis Based on LabVIEW Environment", Mediterranean Electrotechnical Conference, Vol. 1, pg. 495-498, May 1996.
	A41	Spoelder et al., "Virtual Instrumentation: A Survey of Standards and Their Interrelation", Proc. IEEE Instr. And Measurement Tech. Conf., Vol. 1, pp. 676-681, May 1997.
	A42	Srinivasan et al., "LabVIEW program Design for On-Line Data Acquisition and Predictive Maintenance", Proc. Of the 30th Southeastern Symp. On System Theory, pp. 520-524, March 1998.
	A43	Wahidanabanu et al., "Virtual Instrumentation with Graphical Programming for Enhanced Detection and Monitoring of Partial Discharges", Proc. Electrical Insulation Conf. 1997, pp. 291-296, September 1997.
	A44	Fountain: "Software advances in measurement and instrumentation"; IEE Coll. Software Instrumentation; pp. 4/1 to 4/45.

EXAMINER:

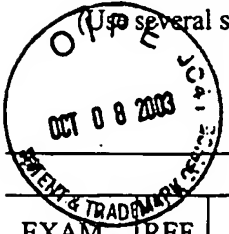
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Information Disclosure Statement--PTO 1449 (modified)
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			Technology Center 2100 APPLICANT: Odom et al.				
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OS	B0	6,219,628	04/17/2001	Kodosky et al.			
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
OS	B1	Choosing Block-Diagram Tools for DSP Design, http://www.bdti.com/articles/info_dspmt95blockdiagram.htm , May 9, 2003, pgs. 1-7.					
	B2	Real-Time Workshop for Use with Simulink, User's Guide, May 1994, 229 pages.					
	B3	Guide to Rapid Prototyping with Simulink, Real-Time Workshop and dSPACE, 1995, 16 pages.					
	B4	Real-Time Interface to Simulink, RTI 30, User's Guide, 1995, 125 pages.					
	B5	Kevin J Gorman and Kourosh J. Rahnamai, "Real-Time Data Acquisition and Controls Using MatLAB", 1995, 4 pages.					
	B6	SPW - MatLAB Co-Simulation Interface Product Data Sheet, 1996, 2 pages.					
	B7	Signal Processing WorkSystem, MatLAB Interface User's Guide, Oct. 1995, 72 pages.					
	B8	Alta Group of Cadence Design Systems, Inc., 1995, 34 pages.					
	B9	Code Generation System Product Data Sheet, 1994, 8 pages.					
	B10	SPW/CGS Porting Kits Product Data Sheet, 11/94, 2 pages.					
	B11	MultiProx for SPW Product Data Sheet, 08/94, 4 pages.					
	B12	DSP ProCoder for SPW Product Data Sheet, 11/94, 4 pages.					
	B13	Xanalog Corporation Sales Manual, January 1987, 8 pages.					
	B14	Available XA-1000 Literature and Its Use, 1986, 2 pages.					
	B15	Xanalog, XA-1000 Programming ICONS, 1986, 26 pages.					
	B16	Xanalog's CAE System: The Fastest AT Alive, Mass High Tech, Vol. 4, No. 22, 08/1988, 1 page.					
	B17	Xanalog The Computer Aided Engineering Workstation Comes to Simulation, Simulation Vol. 47, No. 1, July 1986, 3 pages.					
	B18	Xanalog RT Real Time Analog and Digital I/O, 10/90, 4 pages.					
OS	B19	Xanalog/SC+, 9/90, 4 pages.					
OS	B20	Xanalog Specializing in Workstations for Continuous Dynamic Simulation, 1987, 24 pages.					

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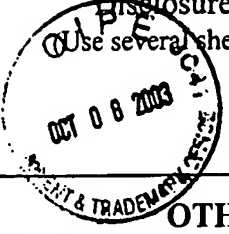
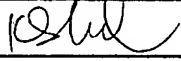
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OTHER ART CONTINUED (Including Author, Title, Date, Pertinent Pages, Etc.)

B21	Xanalog Real-Time User Guide, 1994, 28 pages.
B22	Lee et al., "Gabriel: A Design Environment for Programmable DSPs", 11/7/1988, 13 pages.
B23	Lee et al., "A Design Tool for Hardware and Software for Multiprocessor DSP Systems", May 1989, 4 pages.
B24	Gabriel 0.7 Overview, 1990, 5 pages.
B25	Joseph T. Buck and Edward A. Lee, "Scheduling Dynamic Dataflow Graphs with Bounded Memory Using the Token Flow Model", http://www.synopsys.com/ , 1995, 4 pages.
B26	Edward A. Lee, "Design Methodology for DSP", 1992, 4 pages.
B27	Pino et al., "Interface Synthesis in Heterogeneous System-Level DSP Design Tools, 05/1996, 4 pages.
B28	Jose Luis Pino, Master's Report, "Software Synthesis for Single-Processor DSP Systems Using Ptolemy", May 1993, 48 pages.
B29	Asawaree Kalavade and Edward A. Lee, "A Hardware-Software Codesign Methodology for DSP Applications", 1993, 12 pages.
B30	Pino et al., "Automatic Code Generation for Heterogeneous Multiprocessors", 1994, 4 pages.
B31	Tool Chest continues to Grow, Electronic Engineering Times, 12/15/1995, 2 pages.
B32	Pino et al., "Mapping Multiple Independent Synchronous dataflow Graphs onto Heterogeneous Multiprocessors, 10/1994, 6 pages.
B33	Asawaree Kalavade and Edward A. Lee, "Hardware/Software Co-Design Using Ptolemy - A Case Study, 09/1992, 18 pages.
B34	Pino et al., "Software Synthesis for DSP Using Ptolemy", 1995, 15 pages.
B35	Vol. 1 - Ptolemy 0.7 User's Manual, 1997, 532 pages. —
B36	i-Logix Product Overview, 1996, 52 pages.
B37	Press Release, i-Logix Statemate MAGNUM Supports PCs", 01/31/1997, 2 pages.
B38	Press Release, "i-Logix Signs Reseller Agreement for Virtual Prototypes, Inc.'s VAPS Product Line, 02/11/1997, 2 pages.
B39	Press Release, "i-Logix Introduces Rhapsody, Object-Oriented analysis, Design and Implementation Tool", 02/10/1997, 2 pages.
B40	Statemate/C Product Overview, 1995, 4 pages.
B41	Press Release, "i-Logix and Integrated Systems Link Statemate MAGNUM and MATRIX AutoCode" 01/03/1997, 2 pages
B42	Press Release, "i-Logix and Wind River unveil Industry's First Rapid Prototyping Solution for Testing Embedded Systems at ESC West in San Jose, 09/17/1996, 3 pages.
B43	Press Release, "i-Logix Inc. Endorses Unified Modeling Language, 01/16/1997, 1 page.
B44	International Search Report, Application No. PCT/US02/22895, mailed September 18, 2003.

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B45	Wenban et al. "A Software Development System for FPGA-Based Data Acquisition Systems, 1996, pgs. 28-37.	
B46	Nair C. "PXI/CompactPCI in Embedded Measurement and Control Applications" DEDICATED SYSTEMS MAGAZINE, http://www.omimo.be/Magazine/00q3/2000q3_p056.pdf , Online 2000, pages 56-58.	
B47	Database Inspec. Online, Institute of Electrical Engineers, Stevenage, GB; Dehne T: "Virtual Instrumentation Becomes a Reality" Electrotechnology, Feb.-March 1995, UK, Vol. 6, No. 1, pages 20-22	
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